

539,977

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property  
Organization  
International Bureau(43) International Publication Date  
8 July 2004 (08.07.2004)

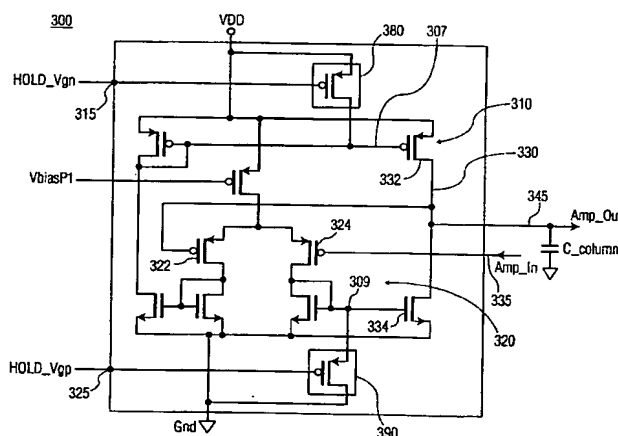
PCT

(10) International Publication Number  
**WO 2004/057564 A1**

- (51) International Patent Classification<sup>7</sup>: **G09G 3/36** (74) Common Representative: **KONINKLIJKE PHILIPS ELECTRONICS N.V.**; Intellectual Property & Standards, c/o Bram, Eric M., P.O. Box 3001, Briarcliff Manor, NY 10510-8001 (US).
- (21) International Application Number: **PCT/IB2003/005988**
- (22) International Filing Date: **11 December 2003 (11.12.2003)**
- (25) Filing Language: **English**
- (26) Publication Language: **English**
- (30) Priority Data: **60/435,248 20 December 2002 (20.12.2002) US**
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- (81) Designated States (national): **AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.**
- (84) Designated States (regional): **ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).**
- Declaration under Rule 4.17:**  
— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for all designations

[Continued on next page]

(54) Title: VIDEO DRIVER WITH INTEGRATED SAMPLE-AND-HOLD AMPLIFIER AND COLUMN BUFFER



(57) Abstract: A video driver includes an input stage (320, 322, 324) adapted to receive a video input signal (aMP-IN), an output stage (330, 332, 334) adapted to provide a video output signal (aMP-OUT) to charge a capacitor, a feedback path (330, 322) between the output stage and the input stage adapted to cause the video output signal to follow the video input signal, and sample-and-hold means (380, 390) for selectively turning off the output stage to disable further charging of the capacitor. In one embodiment, the output stage includes complementary transistors (332, 334) connected in series between a first supply voltage (VDD) and a second supply voltage (Gnd). The sample-and-hold means includes a first sample-and-hold switch (380) connected between a control input (307) of a first one of the complementary transistors and the first supply voltage, and a second sample-and-hold switch (390) connected between a control input (309) of a second one of the complementary transistors and the second supply voltage.

WO 2004/057564 A1



**Published:**

— with international search report

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